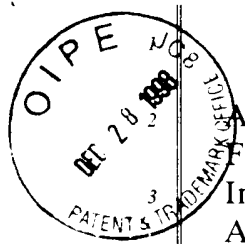


3/11/1999
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 08/530,661
Filing Date September 20, 1995
Inventor Brent Keeth et al.
Assignee Micron Technology, Inc.
Group Art Unit 2814
Examiner N. Kelley
Attorney's Docket No. MI22-356
Title: Semiconductor Memory Circuit

RESPONSE TO JUNE 23, 1998 OFFICE ACTION

To: BOX FEE AMENDMENT
 Assistant Commissioner of Patents and Trademarks
 Washington, D.C. 20231

From: David G. Latwesen (Tel. 509-624-4276; Fax 509-838-3424)
 Wells, St. John, Roberts, Gregory & Matkin P.S.
 601 W. First Avenue, Suite 1300
 Spokane, WA 99201-3828

AMENDMENTS

In the Claims

none.

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JAN 05 1999

GROUP 2100